

	L #	Hits	Search Text	DBs	Time Stamp
1	L1	9451	differential adj amplif\$4 and transistor and supply near voltage	USPAT; EPO; JPO; DERWENT	2002/12/07 15:12
2	L2	41	1 and switchable near2 voltage\$1	USPAT; EPO; JPO; DERWENT	2002/12/07 15:16
3	L3	9410	1 not 2	USPAT; EPO; JPO; DERWENT	2002/12/07 15:17
4	L4	5767	3 and power adj supply	USPAT; EPO; JPO; DERWENT	2002/12/07 15:18
5	L5	4296	4 and switch\$5	USPAT; EPO; JPO; DERWENT	2002/12/07 15:19
6	L6	471	5 and 330/\$.ccls.	USPAT; EPO; JPO; DERWENT	2002/12/07 15:19
7	L7	52	6 and differential adj transistor\$1	USPAT; EPO; JPO; DERWENT	2002/12/07 16:18
8	L8	419	6 not 7	USPAT; EPO; JPO; DERWENT	2002/12/07 15:28
9	L9	390	8 and reference	USPAT; EPO; JPO; DERWENT	2002/12/07 16:15
10	L10	29	8 not 9	USPAT; EPO; JPO; DERWENT	2002/12/07 16:15
11	L11	3825	5 not 6	USPAT; EPO; JPO; DERWENT	2002/12/07 16:18
12	L12	79	11 and differential adj transistors\$1	USPAT; EPO; JPO; DERWENT	2002/12/07 16:19
13	L13	138	11 and differential adj transistor\$1	USPAT; EPO; JPO; DERWENT	2002/12/07 16:26
14	L14	609	6 13	USPAT; EPO; JPO; DERWENT	2002/12/07 16:26
15	L15	3687	5 not 14	USPAT; EPO; JPO; DERWENT	2002/12/07 16:26
16	L16	1740	15 and (FET or MOSFET or CMOS)	USPAT; EPO; JPO; DERWENT	2002/12/07 16:27
17	L17	652	16 and (327/\$.ccls. or 330/\$.ccls. or 323/\$.ccls. or 326/\$.ccls.)	USPAT; EPO; JPO; DERWENT	2002/12/07 16:29
18	L18	2	5168209.pn.	USPAT; EPO; JPO; DERWENT	2002/12/07 17:37
19	L19	2	5563501.pn.	USPAT; EPO; JPO; DERWENT	2002/12/07 17:38
20	L20	46	1 and (switching or switchable or adaptable) adj2 voltage\$1 near suppl\$4	USPAT; EPO; JPO; DERWENT	2002/12/07 17:40
21	L21	13010	differential adj (amplif\$4 or transistor\$1) and (suppl\$4 near2 voltage\$1) and switch\$3	USPAT; EPO; JPO; DERWENT	2002/12/07 18:14
22	L22	3961	21 and (327/\$.ccls. or 330/\$.ccls. or 326/\$.ccls. or 323/\$.ccls.)	USPAT; EPO; JPO; DERWENT	2002/12/07 18:14
23	L23	3309	22 not 17	USPAT; EPO; JPO; DERWENT	2002/12/07 18:15

	L #	Hits	Search Text	DBs	Time Stamp
24	L24	3295	23 not 20	USPAT; EPO; JPO; DERWENT	2002/12/07 18:15
25	L25	3278	24 not 2	USPAT; EPO; JPO; DERWENT	2002/12/07 18:15
26	L26	2812	25 not 6	USPAT; EPO; JPO; DERWENT	2002/12/07 18:15
27	L27	2770	26 not 12	USPAT; EPO; JPO; DERWENT	2002/12/07 18:16
28	L28	2734	27 not 13	USPAT; EPO; JPO; DERWENT	2002/12/07 18:16
29	L29	1180	28 and (MOSFET or CMOS or MOS or FET)	USPAT; EPO; JPO; DERWENT	2002/12/07 18:17
30	L30	1045	29 and transistor	USPAT; EPO; JPO; DERWENT	2002/12/07 18:17
31	L31	497	30 and (control or enable) adj signal	USPAT; EPO; JPO; DERWENT	2002/12/07 18:18